Al-Dhahir 1

IN THE CLAIMS:

1. **(Withdrawn).**

2. **(Currently Amended)** A receiver responsive to an n_o plurality of entry points comprising:

a feedforward filter structure having an $n_o \times n_i$ plurality of FIR filters, each responsive to a signal that is derived from one of said n_o entry points and each developing an output signal that contributes to one of n_i feedforward filter structure outputs;

a feedback filter structure developing n_i feedback signals, the structure having an $n_i \times n_i$ plurality of FIR filters, each being responsive to one of n_i output signals;

a subtractor structure that develops n_i signals from signals of said n_i feedforward filter structure outputs and said n_i feedback signals;

decision logic responsive to said n_i outputs developed by said subtractor structure, for developing said n_i output signals; and

~~The receiver of claim 1 further comprising~~ a sampling circuit interposed between said n_o plurality of entry points and said feedforward filter structure that samples received signal at rate $T_s = \frac{T}{l}$, where l is an integer and T is symbol rate of a transmitter whose signals said receiver receives.

3. **(Original)** The receiver of claim 2 where $l>1$.

4. **(Withdrawn).**

5. **(Withdrawn).**

6. **(Withdrawn).**

7. **(Withdrawn).**